

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	philips.as. and (lin (local adj interconnect adj network)) and serial and parallel and oscillator	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:51
S2	652	(lin (local adj interconnect adj network)) and serial and parallel and oscillator	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:52
S3	220	(lin (local adj interconnect adj network)) and serial and parallel and oscillator and supply and reset	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:52
S4	162	(lin (local adj interconnect adj network)) and serial and parallel and oscillator and supply and reset and buffer	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:22
S5	2703	detect\$3 near5 ((bit baud data) adj rate)	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:58
S6	2	S4 and S5	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:53
S7	1	"6049888".pn.	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:54
S8	1827	detect\$3 near3 ((bit baud data) adj rate)	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:12
S9	0	(lin (local adj interconnect adj network)) same serial same parallel same oscillator same supply same reset same buffer	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:59
S10	44	S8.ti.	US-PGPUB; USPAT	OR	OFF	2006/11/08 09:59
S11	86781	serdes (serial with parallel)	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:00
S12	8	S10 and S11	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:11
S13	3	reset and S12	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:11
S14	29	(lin (local adj interconnect adj network)) and serial and parallel and oscillator and supply and reset and buffer and watchdog	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:22
S15	17	(lin (local adj interconnect adj network)) and serial and parallel and oscillator and supply and reset and buffer and watchdog and battery	US-PGPUB; USPAT	OR	OFF	2006/11/08 10:25
S16	1	"20060059276".pn.	DERWENT	OR	OFF	2006/11/08 10:26
S17	1827	detect\$3 near3 ((bit baud data) adj rate)	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:08
S18	214	S17 and (serdes (serial near2 parallel))	US-PGPUB; USPAT	OR	OFF	2006/11/08 12:33
S19	85	oscillator and S18	US-PGPUB; USPAT	OR	OFF	2006/11/08 12:34
S20	13	uart and S19	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:13
S21	17	S17 and serdes	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:25
S22	8	sci adj uart	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:26
S23	27	(sci (serial adj communication adj interface)) adj (uart (universal adj asynchronous adj receiver adj transmitter))	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:30
S24	13	@ad<"20021109" and S23	US-PGPUB; USPAT	OR	OFF	2006/11/08 14:51
S25	1332	detect\$3 near2 ((bit baud data) adj rate)	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:45
S26	53	S25 same oscillator	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:45

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S27	16	S26 and serial and parallel	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:47
S28	11	@ad<"20021109" and S27	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:45
S29	3	S26 and serdes	US-PGPUB; USPAT	OR	OFF	2006/11/08 11:47
S30	45	S17 and ((mux (multiplexer)) near3 (demux (demultiplexer)))	US-PGPUB; USPAT	OR	OFF	2006/11/08 12:34
S31	15	oscillator and S30	US-PGPUB; USPAT	OR	OFF	2006/11/08 12:34
S32	20440	((voltage power) adj supply) same reset	US-PGPUB; USPAT	OR	OFF	2006/11/08 15:01
S33	1131	rc adj oscillator	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:17
S34	99	S32 and S33	US-PGPUB; USPAT	OR	OFF	2006/11/08 14:51
S35	14	S32 same S33	US-PGPUB; USPAT	OR	OFF	2006/11/08 14:51
S36	11	@ad<"20021109" and S35	US-PGPUB; USPAT	OR	OFF	2006/11/08 15:01
S37	198	((voltage power) adj supply) and reset and S33 and buffer	US-PGPUB; USPAT	OR	OFF	2006/11/08 15:59
S38	157	@ad<"20021109" and S37	US-PGPUB; USPAT	OR	OFF	2006/11/08 15:01
S39	5	"710"/\$.ccls. and S38	US-PGPUB; USPAT	OR	OFF	2006/11/08 15:02
S40	1866	((voltage power) adj supply) and (system adj reset) and buffer	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:07
S41	167	"710"/\$.ccls. and @ad<"20021109" and S40	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:18
S42	101	((voltage power) adj supply) near5 reset near7 (integrated adj circuit)	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:07
S43	0	"710"/\$.ccls. and @ad<"20021109" and S42	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:19
S44	71	@ad<"20021109" and S42	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:07
S45	298	(rc adj oscillator).bsum.	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:19
S46	69	buffer and S45	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:17
S47	56	@ad<"20021109" and S46	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:18
S48	1	"710"/\$.ccls. and @ad<"20021109" and S45	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:20
S49	223	@ad<"20021109" and S45	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:19
S50	17	(rc adj oscillator) near5 advantage	US-PGPUB; USPAT	OR	ON	2006/11/08 16:33
S51	16	@ad<"20021109" and S50	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:21
S52	209	(complete adj message) near5 buffer	US-PGPUB; USPAT	OR	ON	2006/11/08 16:23
S53	190	@ad<"20021109" and S52	US-PGPUB; USPAT	OR	OFF	2006/11/08 16:21

## EAST Search History

S54	8	(complete adj message) near5 buffer near7 byte	US-PGPUB; USPAT	OR	ON	2006/11/08 16:25
S55	116	(complete adj message) near5 buffer near5 (transmit\$4 receiv\$3)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:25
S56	3	(rc adj oscillator) near5 external near5 microprocessor	US-PGPUB; USPAT	OR	ON	2006/11/08 16:37
S57	3	(rc adj oscillator) near5 external near5 (processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:38
S58	33	(rc adj oscillator) near5 (processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:38
S59	69	(rc adj oscillator) near5 (microprocessor processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:40
S60	8	(rc adj oscillator) near7 external near7 (cpu microprocessor processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:42
S61	0	(rc adj oscillator) near5 clock near5 provide near5 (cpu microprocessor processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:42
S62	1	(rc adj oscillator) near5 clock near5 provid\$3 near5 (cpu microprocessor processor controller)	US-PGPUB; USPAT	OR	ON	2006/11/08 16:42
S63	17	(rc adj oscillator) with (outside external) with (cpu microprocessor processor controller)	US-PGPUB; USPAT	OR	OFF	2006/11/08 17:35
S64	415	710/16.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:04
S65	265	710/71.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:08
S66	1905	710/305.ccls.	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:08
S67	1335	detect\$3 near2 ((bit baud data) adj rate)	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:09
S68	2	S67 and S64	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:09
S69	1	S67 and S65	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:09
S70	3	S67 and S66	US-PGPUB; USPAT	OR	OFF	2006/11/09 08:09